

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	50	(US-5717895-\$ or US-5608892-\$ or US-5875466-\$ or US-5511212-\$ or US-5430861-\$ or US-5664149-\$ or US-5255383-\$ or US-5479630-\$ or US-5524234-\$ or US-5535116-\$ or US-5659708-\$ or US-5689680-\$ or US-5860111-\$ or US-5991853-\$ or US-4953121-\$ or US-5274782-\$ or US-5940864-\$ or US-5996049-\$ or US-6018791-\$ or US-6199147-\$ or US-4456952-\$ or US-4899275-\$ or US-4953073-\$ or US-5321698-\$ or US-5345578-\$ or US-5561780-\$).did. or (US-5642486-\$ or US-5692151-\$ or US-5724549-\$ or US-5729712-\$ or US-5732278-\$ or US-5732409-\$ or US-5778433-\$ or US-5826053-\$ or US-5835955-\$ or US-5857183-\$ or US-5875470-\$ or US-5900023-\$ or US-5933856-\$ or US-5940856-\$ or US-5943685-\$ or US-5943684-\$ or US-5946709-\$ or US-5956743-\$ or US-5963974-\$ or US-6070166-\$ or US-6108663-\$ or US-6128639-\$ or US-6148368-\$ or US-6173370-\$). did.	USPAT	OR	ON	2006/09/13 19:26
L2	723	((bypass or bypassed or bypassing) near5 (cache or cached or caching)) with data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:55
L3	1	1 and 2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:27
L4	1399	(bypass or bypassed or bypassing) near5 (cache or cached or caching)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:29
L5	5	1 and 4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:29
L6	4	5 not 3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L7	1968	711/118.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L8	335	711/138.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L9	1116	711/144.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31

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L10	832	711/145.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L11	2314	711/154.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:31
L12	5642	7 or 8 or 9 or 10 or 11	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:32
L13	190	2 and 12	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:32
L14	0	"NOT" near3 (cache or cached or caching)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:33
L15	0	"NOT" with (cache or cached or caching)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:42
L16	138982	(status adj bit) or ((update or updated) with data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:43
L17	108	13 and 16	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:43
L18	39	8 and 17	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:43
L19	14216068	@ad<"20040319"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:44
L20	38	18 and 19	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:44
L21	8	(US-20040117441-\$ or US-20010052053-\$).did. or (US-6922754-\$ or US-6356980-\$ or US-6131155-\$ or US-5918069-\$ or US-5809550-\$ or US-5729713-\$).did.	US-PGPUB; USPAT	OR	ON	2006/09/13 19:59
L22	22	((bypass or bypassed or bypassing) near5 (cache or cached or caching)) with threshold)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:56

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L23	17	19 and 22	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:56
L24	3	17 and 23	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 19:56
L25	3767	((threshold near5 (amount or quantity)) with data)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L26	0	24 and 25	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L27	1	21 and 25	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L28	1	4 and 27	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/09/13 20:00
L29	531	711/118.ccls.	US-PGPUB	OR	ON	2006/09/13 20:01
L30	375	19 and 29	US-PGPUB	OR	ON	2006/09/13 20:01
L31	0	4 and 25 and 30	US-PGPUB	OR	ON	2006/09/13 20:02
L32	8	4 and 16 and 30	US-PGPUB	OR	ON	2006/09/13 20:02
L33	0	25 and 32	US-PGPUB	OR	ON	2006/09/13 20:02

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- #3 ((cache <and> bypass)<in>metadata)
- #4 (((cache <and> bypass)<in>metadata)<AND>(threshold <or> thresholds<in>metadata))
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 Youfeng Wu; Rakvic, R.; Li-Ling Chen; Chyi-Chang Miao; Chrysos, G.; Fang, J.;
[Microarchitecture, 2002. \(MICRO-35\). Proceedings. 35th Annual IEEE/ACM International Symposium on](#)
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- ☐ **2. DHTTP: an efficient and cache-friendly transfer protocol for Web traffic**
 Rabinovich, M.; Hua Wang;
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- ☐ **3. Discretionary caching for I/O on clusters**
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- ☐ **4. Optically assisted Internet routing using arrays of novel dynamically reconfigurable FBG-based correla:**
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- ☐ **6. Predictive techniques for aggressive load speculation**

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-  7. **Energy-aware code cache management for memory-constrained Java devices**
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-  12. **Interoperation of copy avoidance in network and file I/O**
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-  13. **In-system FPGA prototyping of an Itanium microarchitecture**
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-  **11. Scalable high-level caching for parallel I/O**
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Yongwoo Cho; Kyungtae Kang; Joonho Lee; Heonshik Shin;

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